QD-1020HC

Product Highlights

Advanced High Current Multi-site IDDQ Measurement Instrument Supporting Various Test Applications

**FEATURES**

- Wide DUT Supply range: \( V_{DUT} = 0.5\text{V} \text{ to } 7\text{V} \)
- Wide measurement ranges up to 200A
- Typical measurement time: 100 \( \mu \text{s} \)
- High capacitive driving capability: up to 100\( \mu \text{F} \)
- High resolution: 150nA RMS
- 16 or 24-bit digital resolution
- 3-Wire Serial Configuration/Read out Interface
- Cascadable up to 8 QD-1020 units
- On-board data processing capabilities

**APPLICATIONS**

- Multi-site Applications
- ATE Load Board Applications
- ATE Probe Card Applications
- Delta IDDQ Measurements
- Pre & Post Stress Delta IDDQ
- IDDQ Pass/Fail Measurements
- IDDQ Read Out Measurements
- IDDQ Window Comparisons

**DESCRIPTION**

The QD-1020HC is a full featured, configurable quiescent supply high current (IDDQ) instrument, designed for multi-site applications, serving both probe and final test. Up to 8 QD-1020HC units can be cascaded, sharing the same control resources. To save ATE I/O pins, a 3-wire serial control/configuration/read out interface is used to control all instruments. All units can be addressed globally or individually. When sufficient ATE resources are available, instruments can be controlled with independent data outputs as well.

On-board memory and data processing capabilities allow implementing a wide range of basic and advanced current based test strategies and allow offloading the IDDQ data processing and decision making from the ATE to the instrument. The QD-1020HC is offered either with a 16 or 24 bit architecture and related measurement resolution/repeatability, and can have a horizontal or a vertical form factor.

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The QD-1020HC is designed to be inserted between the Automated Test Equipment (ATE) device power supply and the supply pin(s) of the Device Under Test (DUT). There is no need to remove the local decoupling capacitors. Its unique design ensures transparency to both the ATE and DUT, under all conditions. The unit can drive high capacitive loads (up to several tens of \( \mu \text{F} \)). The QD-1020HC offers the capability to perform accurate and highly repeatable high-speed (up to 10kHz) quiescent supply current measurements with sub-\( \mu \text{A} \) resolution/repeatability.

The instrument can have measurement ranges up to 2A when used without external sense element or up to 200A when used in combination with an external sense element. The serial output provides the Pass/Fail flag and/or the measured or processed IDDQ value with a 16 or 24-bit resolution. The QD-1020HC requires only a single positive supply, and allows a user programmable (0.5 to 7V) DUT supply level.

![Diagram of QD-1020HC Application](Figure 1. QD-1020HC Application)
The QD-1020HC has two main operating modes: **bypass mode** and **measurement mode**. During bypass, the instrument provides a low resistance path between ATE supply and DUT. During measurement, the actual measurement(s) take(s) place. The instrument can be programmed during bypass mode. The programming operation allows selecting the measurement approach and to set the pass/fail level(s). A simple programming protocol is used.

The normal measurement cycle consists of a settling period (typ. 100µs) followed by a capture, processing and read-out period (typ. 15µs). When in measurement mode the instrument is acting as DUT power supply. When during measurement mode the measured current is out of the instrument’s measurement range, then the QD-1020HC automatically switches back to bypass mode, meanwhile indicating a fail situation. Figures 1 and 2 show a general application diagram as well as a typical measurement cycle.

**TYPICAL APPLICATIONS**

The QD-1020HC is designed to be inserted between the Automated Test Equipment (ATE) device power supply and the supply pin(s) of the Device Under Test (DUT). There is no need to remove the local decoupling capacitors. Its unique design ensures transparency to both the ATE and DUT, under all conditions.

The QD-1020HC is designed for multi-site DUT instrument applications. Each instrument has a 3-bit hardware address (hardwired on the DUT loadboard) that allows assigning a unique address to each instrument. As such, each instrument can be accessed individually for setting references and for data readout. Up to 8 instruments can be cascaded (connected in parallel) and can all be controlled using one 3-wire serial interface. All control lines are connected in parallel. This application mode saves ATE tester channels but causes additional readout time as the data stream is provided sequentially on the data output line DOUT.

The instrument can also be configured to provide the data of all instruments simultaneously independent of its hardware address. This requires that all instrument data outputs be connected to separate ATE channels.

**ELECTRICAL SPECIFICATIONS**

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<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<tr>
<td>VDD</td>
<td>Positive Supply Voltage</td>
<td>+4.5</td>
<td>+5.0</td>
<td>+5.5</td>
<td>V</td>
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<td>CMR</td>
<td>Current Measurement Range</td>
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<td>500</td>
<td>2000</td>
<td>mA</td>
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<tr>
<td>VOUT</td>
<td>OUT Supply Voltage</td>
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<td>3 - 5</td>
<td>7</td>
<td>V</td>
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<td>gIDDQ</td>
<td>Single Sample Resolution [1]</td>
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<td>20</td>
<td>80</td>
<td>μA RMS</td>
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<td>μF</td>
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<td>V(1900)</td>
<td>V/I Conversion Ratio</td>
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<td>10</td>
<td>100</td>
<td>V/A</td>
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<td>LIDDQ</td>
<td>Transient Current in Bypass Mode</td>
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<td></td>
<td>A</td>
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<td>VNT</td>
<td>Voltage drop between VDUT and DUT pins</td>
<td>50</td>
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<td></td>
<td>mV</td>
</tr>
</tbody>
</table>

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[1] Configuration dependant, the values listed are for a CL=1µF optimised unit.
[2] The maximum measurement time is dependent on the number of samples taken: 116µs @ 1 sample, 146µs @ 4 samples, 260µs @ 16 samples.

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Due to continuous pursuit of innovation, the technical specifications listed are subject to change without notice.